Amendments To The Specification

Please replace the paragraph beginning on page 6, line 23 with the following amended paragraph:

FIG. 18 is a block diagram illustrating the addressing software that addresses and drives the emitter lines and gate wires of the FED devices of several embodiments of the invention; and [[.]]

Please replace the paragraph beginning on page 9, line 9 with the following amended paragraph:

Referring next to FIG. 5, a perspective view is shown of a cathode plate of a field emission display (FED) including emitter lines and trenches formed within the cathode substrate in accordance with another embodiment of the invention. In this embodiment, a cathode plate 500 includes a cathode substrate 502 having trenches 504 formed within a top surface of the cathode substrate 502. Within each trench 504 is deposited a respective emitter line 406 as described above. The trenches 504 are etched into the cathode substrate 502, and thus, have a low aspect ratio. The trenches 504 function as isolation barriers between respective emitter lines 406; thus, the trenches 504 may also be referred to generically as "in-laid linear isolation barriers". The trenches 504 provide field isolation and reduce electron spreading of the electrons emitted from the emitter lines 406. Also, the trenches provide mechanical support for gate wires of a gate frame as is further described below. It is noted that in some embodiments, more than one emitter line 406 is formed within a respective trench 504, such as illustrated in the cathode plate 1900 of FIG. 19 where two emitter lines 406 are located within each trench 504.

Please ADD the following new paragraph after the paragraph beginning on page 6, line 23:

FIG. 19 is a perspective view of an alternative embodiment of the cathode plate of FIG. 5 including more than one emitter line within a trench.